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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: PARK, Woon-Yong; LEE, Won-Hee; KIM Il-Gon; LIM, Seung-Taek;  
SONG, You-Lee; and JUN, Sahng-Ik  
Assignee: Samsung Electronics Co., Ltd.

Title: THIN FILM TRANSISTOR ARRAY PANEL, MANUFACTURING  
METHOD THEREOF, AND MASK THEREFOR

Application No.: 10/771,278 Filing Date: February 2, 2004

Examiner: ANDUJAR, Leonardo Group Art Unit: 2826

Docket No.: AB-1353 US Confirmation No.: 2069

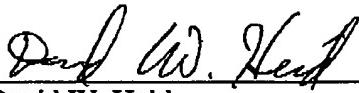
San Jose, California  
January 5, 2007

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Dated: January 5, 2007

  
David W. Heid  
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PAGE 1/3 \* RCVD AT 1/5/2007 2:51:21 PM [Eastern Standard Time] \* SVR:USPTO-EFXRF-2/22 \* DNIS:2738300 \* CSID:408 392 9262 \* DURATION (mm:ss):01:16 Application No. 10/771,278

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San Jose, California  
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**COMMUNICATION**

Dear Sir:

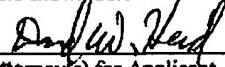
This communication is in response to the Notice Requiring Extension of Time Fee mailed November 9, 2006. In view of the official closing of the U.S. Patent and Trademark Office (USPTO) on October 9, 2006 in observance of Columbus Day, Applicants believe that the Response to Restriction Requirement submitted by Express Mail on October 10, 2006 was timely filed. A copy of Express Mail Label No EV 901 346 189 US is attached.

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The Examiner is invited to call the undersigned at (408) 392-9250 with any questions regarding the above-identified application.

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\_\_\_\_\_  
Attorney(s) for Applicant

Jan 5, 2007  
Date

Respectfully submitted,



David W. Heid  
Attorney for Applicants  
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